EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	487	712/226.ccls.	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:22
L2	329668	flash	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:22
L3	42651	dma	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:22
L4	62370	sram	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:22
L5	11069	2 and 3	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:23
L6	3327	4 and 5	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:23
L7	689950	microprocessor or microcomputer or (integrated adj circuit)	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:24
L8	2484	6 and 7	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:24
L9	689	2 with 3	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:24
L10	133	4 with 9	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:24
L11	101	7 and 10	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:24
L12	18786	charge adj pump	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:24
L13	973	2 with 12	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:25
L14	299	2 near3 12	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:25
L15	15294	data adj port	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:25
L16	591	(instruction or code) adj port	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:25
L17	201	15 and 16	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:25
L18	2000	embedd$5 near3 flash	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:26
L19	78	8 and 18	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:26
L20	2	14 and 17	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:26
L21	78	8 and 18	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:26

L22	13	12 with 18	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:26
L23	100686	(interface or path) near3 (exten$5 or widen$3)	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:27
L24	13	2 with 22	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:27
L25	58	2 near3 23	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:27
L26	210	2 with 23	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:27
L27	1074	flash with save	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:28
L28	1	slave near2 interface with 15	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:28
L29	190	flash near3 slave	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:28
L30	626	coprocessor near3 interface	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:28
L31	0	flash near3 30	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:29
L32	8	flash with 30	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:29
L33	8853	configuration adj register$1	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:29
L34	1319	flash with fpga	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:29
L35	0	coprocessor with 34	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:29
L36	30	coprocessor and 34	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:30
L37	3	configuration adj register$1 and 36	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:30
L38	54286	variable adj (width or length)	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:32
L39	14822	7 and 38	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:32
L40	3445	39 and flash	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:32
L41	418	6 and 40	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:32
L42	1627	variable adj (width or length) near3 instruction$1	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:32
L43	418	40 and 41	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:32

EAST Search History

L44	98	41 and fpga	US-PGPUB; USPAT	OR	OFF	2007/12/06 18:33
L45	741	fpga	EPO; JPO; IBM_TDB	OR	OFF	2007/12/06 18:33
L46	22980	flash	EPO; JPO; IBM_TDB	OR	OFF	2007/12/06 18:33
L47	18	46 and 45	EPO; JPO; IBM_TDB	OR	OFF	2007/12/06 18:33
L48	477	coprocessor	EPO; JPO; IBM_TDB	OR	OFF	2007/12/06 18:33
L49	0	47 and 48	EPO; JPO; IBM_TDB	OR	OFF	2007/12/06 18:33
L50	134	embedd$3 and flash	EPO; JPO; IBM_TDB	OR	OFF	2007/12/06 18:34
L51	27908	(slave or coprocessor)	EPO; JPO; IBM_TDB	OR	OFF	2007/12/06 18:34
L52	1	50 and 51	EPO; JPO; IBM_TDB	OR	OFF	2007/12/06 18:34